

Prof. AVIRENI SRINIVASULU



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EDUCATION

- **Ph.D**, VLSI Design, **Birla Institute of Technology**, Mesra, Ranchi, India, 2010.
- **M.S**, Software Systems, **Birla Institute of Technology and Science, Pilani**, India, 1998.
- **M.Eng**, Power Electronics, **Gulbarga University**, Gulbarga, India, 1991.
- **B.Tech**, Electronics & Communication Engineering (E.C.E), **Sri Venkateswara University**, Tirupati, India, 1986.

PROFESSIONAL EXPERIENCE

Professor of E.C.E, JECRC University, Jaipur, India, from Nov. 09, 2017 to till date.

Professor of E.C.E, V.F.S.T.R University, Vadlamudi, India, from Jun. 6, 2011 to Nov. 08, 2017.

- **Dean (R & D)**, from Jul. 16, 2013 to Nov. 15, 2016.
- **Member**, Board of Management, from Jan. 25, 2014 to Oct. 06, 2016.
- **Member**, Academic Council, from Jan. 25, 2014 to Nov. 08, 2017.

Reader, from Apr. 1, 2009 to May. 23, 2011 and

Assistant Professor, Dept of E.C.E, **Birla Institute of Technology, Mesra**, Ranchi, from Jan. 13, 2007 to Mar. 31, 2009.

- **VLSI Design Research Center Head** from 01-04-2009 to 23-05-2011.

Associate Professor, from Aug. 07, 1995 to Jan. 05, 2007 and

Assistant Professor, from Oct. 04, 1991 to Aug. 6, 1995, Dept of E.C.E, **K. S. R. M. College of Engineering (KSRMCE)**, Kadapa, A.P, India.

- **General Warden** from Jul.1994 to Nov.1998, Hostel Kandula.

✚ From Nov. 2, 1999 to May. 26, 2003 worked as an Asso. Professor in the Dept of Commn. Tech, Faculty of Electrical Engineering, **Defence University Engineering College**, Ministry of National Defence, **Government of Ethiopia, ETHIOPIA** (*being on leave from KSRMCE, Kadapa*).

- ✚ **Member, Academic Council**, from May. 2000 to Mar. 2001.

✚ From May. 30, 2003 to Dec. 31, 2006 worked as a senior teaching faculty member in the Dept of E.C.E, Faculty of Technology, **Kigali Institute of Science and Technology University**, Ministry of Education, **Government of Rwanda (UNDP)**, Kigali, **RWANDA** (*being on leave from KSRMCE, Kadapa*).

- **Head of the Department** (i/c) in the absence of the HEAD.

✚ **Visiting Faculty Member**, Department of Information Technology and Management, **National University of Rwanda**, Butare, **RWANDA**, from Mar. 01, 2004 to Dec. 31, 2006.

✚ **Visiting Faculty Member**, Department of Computer Science, **Sri Venkateswara Degree College**, Balaji Nagar, Kadapa, Andhra Pradesh, India, from 1998 to 1999.

Lecturer, Dept. of E.C.E, **Guru Nanak Dev Polytechnic**, Bidar, Karnataka, India, from Sep. 21, 1987 to Sep. 20, 1989.

Associate Lecturer, Dept. of E.C.E, **T. G. L. G. Polytechnic**, Adoni, A.P, India, from Mar. 15, 1987 to Sep. 19, 1987.

PATENT

Patent Filed No.1237/CHE/2014: Avireni Srinivasulu, Posani Vijaya Lakshmi and Musala Sarada, "A novel high speed, low power three input static CMOS Exclusive-OR logic gate circuit", 10th Mar 2014, India.

LIST OF PUBLICATIONS

International Journals - **32**

International/National Conference Proceedings – **50 + 1** Book Chapter

Scopus Link: <https://www.scopus.com/authid/detail.uri?authorId=57189312829>

Google Scholar Link: http://scholar.google.co.in/citations?hl=en&user=BAT7YPAAAAAJ&view_op=list_works

MEMBERSHIP/FELLOWSHIP OF PROFESSIONAL SOCIETIES

(1). Senior Member, IEEE (2). Senior Member, IACSIT (3). Life Member, IE(I) (4). Life Member, ISTE

EDITOR-IN-CHIEF

1. VFSTR Journal of Science, Technology, Engineering & Management (STEM Journal), <http://www.vignanuniversity.org/vfstrstem/editorial.html>
2. International Journal of Computer Science and Applications (IJCSA) (from 2012 to 2015), <https://www.destechpub.com/product/ijcsa/>

REVIEWER

International Journals

1. IEEE Transactions on Circuits and Systems II - (*ongoing*). Ref: Yong Lian, Editor-in-Chief and Tong Ge, Asso. Editor
2. Journal of Circuits, Systems, and Computers (*ongoing*), Ref: Prof. Maurizio Valle, Associate Editor
3. International Journal of Electronics (*ongoing*). Ref: Dr. Alaa Abunjaileh, Associate Editor
4. Circuits, Systems & Signal Processing (Springer) – 2013, 2014 and (*ongoing*). Ref: Prof. P K. Meher, and Prof. Raj Senani, Associate Editors
5. Analog Integrated Circuits & Signal Processing (ALOG) (Springer) (*ongoing*). Ref: Piero Malcovati, Editor-in-Chief
6. International Journal of Circuit Theory and Applications -2011, 2016 Ref: Prof. Igor Filanovsky, Review Editor.
7. International Journal of Electronics and Communications (AEU)-2015, Ref: Kailiang Chen Ph.D, Area Editor, Elsevier
8. TELKOMNIKA, 2014. Ref: ToleSutikno, Universitas Ahmad Dahlan.
9. WSEAS Transactions on Circuits and Systems, Ref: Rayna Moskova & Alkis Polyraakis
10. International Journal of Design, Analysis and Tools for Circuits and Systems (Hong-Kong)–2012, 2013. Ref: Prof. K. L. Man, Editor in Chief.
11. IEEE Transactions on Instrumentation and Measurements Journal-2011 Ref: Ms. Cam Ingelin, Transactions Administrator.
12. Microelectronics Journal -2009, Ref: Bernard Courtois, Editor-in-Chief.

International Conference Proceedings

1. International Conference on Electronics, Computers and Artificial Intelligence, 29 Jun. to 01 Jul. 2017, Targoviste, Romania.
2. International Symposium on Circuits And System, 28-31 May. 2017, Baltimore, MD, USA.
3. The 5th World Conference on Information Systems and Technologies, 11 - 13 Apr. 2017, Madeira, Portugal.
4. The 4th World Conference on Information Systems and Technologies, 22 - 24 Mar.2016, Recife, PE, Brazil.
5. International Conference on Electronics, Computers and Artificial Intelligence, Jun. 25 - 27, 2015, Bucarest, ROMANIA.
6. The World Conference on Information Systems and Technologies, April 1-3, 2015, Azores, PORTUGAL.
7. International Conference on Devices, Circuits and Communications 2014, 12-13 Sept. 2014, Dept. of E.C.E, Birla Institute of Technology, Mesra, Ranchi, INDIA.
8. International Conference on Future Communication, Information and Computer Science, May 22-23, 2014, Beijing, CHINA.
9. International Conference on Computer Science and Application, Jan. 14 to 15, 2014, Indiana, USA.
10. The World Conference on Information Systems and Technologies, Apr. 15-18, 2014, Madeira, PORTUGAL.
11. International Conference on Computer, Networks and Communication Engineering, May. 23-24, 2013, Beijing, CHINA.
12. The World Conference on Information Systems and Technologies, Mar. 27-30, 2013, Algarve, PORTUGAL.
13. International Conference on Communication, Circuits and Systems, Oct. 5-7, 2012, KIIT University, Bhubaneswar, Odisha, INDIA.
14. International Conference on Devices & Communications, Feb. 24-25, 2011, BIT-Mesra. Birla Institute of Technology, Mesra–835 215, Jharkhand, INDIA.
15. International Conference on Communication, Computing & Security, 12-14 Feb. 2011, NIT Rourkela, National Institute of Technology, Rourkela-769008, Odisha, INDIA.

EDITORIAL BOARD MEMBER

1. Microelectronics and Solid State Electronics Journal
Microelectronics and Solid State Electronics is an international peer-reviewed journal.
Address: 1200 Rosemead Blvd, STE D #105, Rosemead, CA, 91731, USA
2. Scientific Committee and Editorial Review Board Member
World Academy of Science, Engineering and Technology (**WASET**)

INVITED TALKS/SPEECHES

National – 13

International - 2

1. **Session Chair** and **Invited Speaker**, *IEEE International Conference on Electronics, Computers and Artificial Intelligence* (IEEE ECAI 2017), 29 June - 01 July, 2017, University Valahia Targoviste, Targoviste, **Romania**.
2. **Invited Speaker** and **Session Chair** of the 2013 *IEEE International SoC Design Conference* (IEEE ISOC-2013), November 17th-19th, 2013, Busan, **South Korea**.

WORKSHOPS/CONFERENCES ORGANISED

1. *Co-Convener*, A two day work shop on System Design Using Xilinx FPGAs' VFSTR University, Foundation for Science, Technology & Research, Vadlamudi-522 213, Guntur, A.P, India, dated: 9 - 10th July 2013.
2. *Program Committee Chair & Technical Committee Member*, National Conference on VLSI, Signal Processing & Communications (NCVSComs12), Dept. of ECE, VFSTR University, 10 -11th February 2012.
3. *Convener*, VLSI design workshop, VFSTR University, dated: 6 -7th February 2012.

AWARDS

- ✦ **Best Paper Award**, "Q-factor and BER Performance Analysis of Inter-satellite Optical Wireless Communication Using 26 Transponders," in the *IEEE International Conference on Intelligent Communication and Computational Techniques* (IEEE ICICT 2017), Manipal University, Jaipur, India, Dec 22-23, 2017.
- ✦ **Certificate of Appreciation**, "Contribution to Research in Electronics & Communication Engineering", *Engineers Day Celebrations-2016*, VFSTR University (Vignan's University), Vadlamudi, India, 15th Sept. 2016.
- ✦ **Best Paper Award**, "A low power encoder for a 5-GS/s 5-bit Flash ADC," in the *2014 IEEE International Conference on Advanced Computing* (IEEE ICoAC 2014), Madras Institute of Technology (MIT-Madras), Chennai, India, Dec. 17-19, 2014.
- ✦ **Best Paper Award**, "High performance hardenable memory cell using 7-Transistors", in the *2014 IEEE International Conference on Advanced Computing* (IEEE ICoAC 2014), Madras Institute of Technology (MIT-Madras), Chennai, India, Dec. 17-19, 2014.
- ✦ **Best Performing Employee 2003-04**, Kigali Institute of Science and Technology University (University of Rwanda), Government of Rwanda, Kigali, RWANDA.

RESEARCH SUPERVISION

Masters Level (M.E/M.Tech) - 31

Ph.D Supervision: Awarded: 3, Ongoing: 4

Awarded

1. Musala Sarada, "Design and development of full swing, high speed Ex-OR and Ex-NOR logic circuits using MOSFETS/FinFETS/CNTFETS", *V.F.S.T.R University*, Vadlamudi, A.P, Awarded in 2018.
2. Vallabhuni Vijay, "Second Generation Differential Current Conveyor (DCCII) and its applications", *V.F.S.T.R University*, Vadlamudi, A.P, Awarded in 2017.
3. Pittala Chandra Sekhar, "Some new Operational Trans-Resistance Amplifier based Sinusoidal/ Square Waveform Generators," *V.F.S.T.R University*, Vadlamudi, A.P, Awarded in 2016.

Ongoing

1. P. V. S Murali Krishna, *Birla Institute of Technology*, Mesra, Ranchi, India.
2. Jithendra Kumar Saini, *Manipal University*, Jaipur, Rajasthan, India, (*Co-Guide*).
3. Syed Zahiruddin, *V.F.S.T.R University*, Vadlamudi, A.P, India.
4. Posani Vijaya Lakshmi, *V.F.S.T.R University*, Vadlamudi, A.P, India.

R & D PROJECTS/DST PAC MEETINGS

1. **Research Seed Grant**, "Design, implementation and fabrication of Error Detection and Correction Decoder using low voltage high performance full swing EX-OR/Ex-NOR gates" and also for the establishment of *Centre of Excellence in VLSI Design Systems*, JECRC University, Jaipur, Rs 20 Lakhs, 2017-18.
2. **Research Seed Grant**, "Design and fabrication of novel high speed, low power three input static CMOS Exclusive-OR logic gate circuits", and also for the establishment of *VLSI Design Research Centre*, V.F.S.T.R University, Guntur, Rs 15 Lakhs, 2011-12.
3. **Chief Coordinator**, Science Engineering and Research Board (SERB) "Start up Grant for Young Scientists in Earth and Atmospheric Sciences" 28th-30th June, 2015 at V.F.S.T.R University, Vadlamudi, Guntur-522 213 Andhra Pradesh (DST Funds Rs 8.50 Lakhs).
4. **Convener**, Department of Science & Technology, Govt. of India, PAC meeting of expert sub-committee on Women Technology Parks in V.F.S.T.R University, Vadlamudi, dated: 8th to 11th May 2013, (DST Funds Rs 5.26 Lakhs).

PERSONAL DETAILS

Date of Birth	26-02-1963 (Twenty Six-February-Nineteen Sixty Three)
Sex	Male
Marital Status	Married
Nationality	INDIAN
Permanent Address	Thurimella (Post), Giddalur (Taluka), Prakasam (Dist), A.P, INDIA.
Spouse &	Mrs. Thaya Madhavi, M.B.A (BIT-Mesra), PhD (Pur. JNU-Jaipur)
Children's Name	(1) Mr. Avireni Bhargav (2) Mr. Avireni Varun

Dr. Avireni Srinivasulu