

## **Resume of Dr. Dipankar Pal**

Dr. Dipankar Pal was born 1961 in Chandannagar, a former French-colony, which unified with India soon after independence in 1947. He stood 2<sup>nd</sup> in order of merit in his school leaving board examination. He completed his B. Tech. in 1<sup>st</sup> class, in Electrical Engineering (1984) from Indian Institute of Technology, and PhD in Electronics & Telecommunication Engineering (1997) from Jadavpur University, Calcutta, India.

He did his postdoctoral research in two different universities of United Kingdom – the University College of London (2007-2008) and University of Bath (2003-2006). His area of research includes Analogue and Mixed Signal Circuit Design, Wet Electronics, ASIC and VLSI. He has designed a number of chips in analog and mixed signal systems.

He has been a Professor since June 2005, and currently is a Professor of Microelectronics in the Birla Institute of Technology & Science, Pilani (K. K. Birla Goa Campus). In one of his earlier positions he also served as Director of the North Eastern Regional Institute of Science & Technology (NERIST) – a central university, under the Ministry of Human Resource Development, Government of India.

He has guided many PhD scholars and has published over 50 research papers in peer reviewed journals and conferences. He is also a regular reviewer in journals like IEEE Transactions on Circuits & Systems I & II, IET Devices, Circuits & Systems and International Journal of Electronics among others. He is a Senior Member of IEEE since 2008 (member since 1999).